

Delegation - Spain



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Javier Galindo received the M.Sc. in Telecommunication Eng. from the Univ. of Zaragoza (Spain) and the M.Sc. and Ph.D. degrees in Electronic Eng. from the Univ. Politècnica de Catalunya (Spain) in 2007, 2015 and 2021 respectively. He got the SSP Postgraduate from the International Space Univ. (FloridaTech and NASA KSC, USA) in 2012.

He spent more than 7 years at CERN (Geneva, Switzerland) conducting research on Low Level RF systems for LINAC4 and the SPS and LHC synchrotrons, and he is now active in Detector technologies within the ECFA DRD7 collaboration and the IAXO/RADES collaboration. He also got a two-year fellowship (Spanish Science Ministry) researching on Microelectronics Digital Design at the European Space Agency (ESA), ESTEC (The Netherlands). Previously he worked on MOEMS devices at EPFL and the private sector in Switzerland.

He joined the Instituto Tecnológico de Aragón (ITA) in Spain in 2022 as senior PostDoc, where he drives the developments on Microelectronic Digital Design and RF systems within the Electrical Technologies Division. His interests are microelectronics, gateway/firmware design, feedbacks and signal processing for RF systems in Particle Accelerators and Scientific Instrumentation. He has contributed to more than 10 competitive R&D projects, both at the national and European levels, and is author of several scientific journal papers and international conference contributions.